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IN THE CLAIMS

1. (Currently Amended) A fault processing method in a computer system having a computer and a manager connected to each other through an I/O bus, comprising ~~transmitting an~~ the steps of:

generating an I/O bus signal which makes the I/O bus fail; and

transmitting said I/O bus signal from said manager to an ~~the I/O bus manager in said computer at a predetermined point of time to inform said I/O bus manager of occurrence of~~ generate an I/O bus fault to thereby make said an I/O bus manager in said computer initialize said I/O bus, and then informing a CPU in said computer of said I/O bus fault as an ~~interruption~~ interrupt to be processed by an OS operated by said CPU.

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2. (Original) A fault processing method in a computer system according to claim 1, wherein said predetermined point of time is a point of time when a fault occurs in said computer.

3. (Original) A fault processing method in a computer system according to claim 1, wherein said predetermined point

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of time is a point of time when an incorrect data is transmitted from said computer to said manager.

4. (Currently Amended) A fault processing method in a computer system according to claim 1, wherein said OS carries out fault processing in response to ~~said interruption~~
interrupt.

5. (Original) A fault processing method in a computer system according to claim 1, wherein said predetermined point of time is a point of time when said computer does not update contents of a predetermined storage device within a predetermined time.

6. (Currently Amended) A computer system comprising a computer, a manager, and an I/O bus for connecting said computer and said manager to each other, wherein which includes a CPU, an I/O bus, and an I/O bus manager, and a manager which connected to said computer via said I/O bus, wherein said manager transmits generates an I/O bus signal which makes the I/O bus fail and transmits said I/O bus signal to an said I/O bus manager in said computer at a

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predetermined point of time to inform said I/O bus manager of occurrence of generate an I/O bus fault, and wherein said I/O bus manager initializes said I/O bus in response of reception to occurrence of said I/O bus signal fault, and then informs a said CPU in said computer of said I/O bus fault as an interruption interrupt to be processed by an OS operated by said CPU.

7. (Original) A computer system according to claim 6, wherein said predetermined point of time is a point of time when a fault occurs in said computer.

8. (Original) A computer system according to claim 6, wherein said predetermined point of time is a point of time when an incorrect data is transmitted from said computer to said manager.

9. (Currently Amended) A computer system according to claim 6, wherein said OS carries out fault processing in response to said interruption interrupt.

10. (Original) A computer system according to claim 6, wherein said predetermined point of time is a point of time

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when said computer does not update contents of a predetermined storage device within a predetermined time.

11. (New) A computer system according to claim 10, wherein said predetermined point of time is determined in accordance with an instruction from an apparatus connected through said manager and a network.

12. (New) A computer system comprising:
a computer; and
a management computer,
wherein said computer comprises a CPU, an I/O bus, and an I/O bus manager,

wherein said computer and said management computer are connected to each other by said I/O bus,

wherein said management computer is adapted to generate a signal which makes said I/O bus fail and to output said signal to said I/O bus, and

wherein said I/O bus manager is adapted to detect an I/O bus fault which is caused by said signal, initialize said I/O bus, and inform said CPU of said I/O bus fault as an interrupt to be processed by an OS executed by said CPU.

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13. (New) A computer system according to claim 12, wherein said management computer has an interface which is connected to an external device via a network, and wherein said management computer outputs said I/O signal when said management computer receives an instruction from said external device via said interface.

14. (New) A computer system according to claim 13, wherein said management computer unlocks said I/O bus before outputting said I/O signal if said I/O bus is locked.

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15. (New) A computer system according to claim 14, wherein said computer has a memory and a storage device, and wherein said CPU sends data stored in said memory to said storage device when said CPU receives information of said I/O bus fault.